

G.PULLA REDDY ENGINEERING COLLEGE (Autonomous) :: KURNOOL
Four Year B.Tech Degree Examination: NOVEMBER- 2024
ELECTRONICS & COMMUNICATION ENGINEERING DEPARTMENT
B. Tech ECE VII- SEMESTER (S-20)
Scheme & Schedule for Regular Practical Examinations

<u>BATCH</u>	<u>REGD. NOS</u>							<u>TOTAL</u>
A	229X5A04L3	229X5A04L4	229X5A04L6	229X5A04L7	229X5A04L8	229X5A04L9	229X5A04M0	
	229X5A04M1	229X5A04M2	229X5A04M3	229X5A04M4	229X5A04M5	229X5A04M6	229X5A04M7	
	229X5A04M8	229X5A04M9	229X5A04N0	229X5A04N1	229X5A04N2	229X5A04N3	229X5A04N4	
	219X1A0401	219X1A0402	219X1A0403	219X1A0404	219X1A0405	219X1A0406	219X1A0407	
	219X1A0408	219X1A0409	219X1A0410	219X1A0411	219X1A0412	219X1A0413	219X1A0415	[35]
B	219X1A0416	219X1A0417	219X1A0418	219X1A0419	219X1A0420	219X1A0421	219X1A0422	
	219X1A0423	219X1A0424	219X1A0426	219X1A0427	219X1A0428	219X1A0429	219X1A0430	
	219X1A0431	219X1A0432	219X1A0433	219X1A0434	219X1A0435	219X1A0436	219X1A0437	
	219X1A0438	219X1A0439	219X1A0440	219X1A0441	219X1A0442	219X1A0443	219X1A0444	
	219X1A0445	219X1A0446	219X1A0447	219X1A0448	219X1A0449	219X1A0450	219X1A0451	[35]
C	219X1A0452	219X1A0453	219X1A0454	219X1A0455	219X1A0456	219X1A0457	219X1A0458	
	219X1A0459	219X1A0460	219X1A0461	219X1A0462	219X1A0463	219X1A0464	219X1A0465	
	219X1A0466	219X1A0467	219X1A0468	219X1A0469	219X1A0470	219X1A0471	219X1A0472	
	219X1A0473	219X1A0474	219X1A0475	219X1A0476	219X1A0477	219X1A0478	219X1A0479	
	219X1A0480	219X1A0481	219X1A0482	219X1A0483	219X1A0484	219X1A0485	219X1A0486	[35]
D	219X1A0487	219X1A0488	219X1A0489	219X1A0491	219X1A0492	219X1A0493	219X1A0494	
	219X1A0495	219X1A0496	219X1A0497	219X1A0498	219X1A0499	219X1A04A0	219X1A04A1	
	219X1A04A2	219X1A04A3	219X1A04A4	219X1A04A5	219X1A04A6	219X1A04A7	219X1A04A8	
	219X1A04A9	219X1A04B1	219X1A04B2	219X1A04B3	219X1A04B4	219X1A04B6	219X1A04B7	
	219X1A04B8	219X1A04B9	219X1A04C0	219X1A04C1	219X1A04C2	219X1A04C3	219X1A04C6	[35]
E	219X1A04C7	219X1A04C8	219X1A04C9	219X1A04D0	219X1A04D1	219X1A04D2	219X1A04D3	
	219X1A04D4	219X1A04D5	219X1A04D6	219X1A04D7	219X1A04D8	219X1A04D9	219X1A04E1	
	219X1A04E2	219X1A04E3	219X1A04E4	219X1A04E5	219X1A04E7	219X1A04E8	219X1A04E9	
	219X1A04F0	219X1A04F3	219X1A04F4	219X1A04F5	219X1A04F6	219X1A04F8	219X1A04F9	
	219X1A04G0	219X1A04G1	219X1A04G2	219X1A04G3	219X1A04G6	219X1A04G7	219X1A04G8	[35]
F	219X1A04G9	219X1A04H0	219X1A04H1	219X1A04H2	219X1A04H3	219X1A04H4	219X1A04H5	
	219X1A04H6	219X1A04H7	219X1A04H8	219X1A04H9	219X1A04J0	219X1A04J1	219X1A04J2	
	219X1A04J3	219X1A04J4	219X1A04J5	219X1A04J6	219X1A04J8	219X1A04J9	219X1A04K0	
	219X1A04K1	219X1A04K2	219X1A04K3	219X1A04K4	219X1A04K5	219X1A04K6	219X1A04K7	
	219X1A04K8	219X1A04L0	219X1A04L1	219X1A04L2	209X1A0421			[33]

Total 208

Name of the Practical examination	Venue	Date	8.30AM-11.30AM	10.30AM-1.30PM	12.30PM-3.30PM	2.30PM-5.30PM
DATA SCIENCE (DS(P))	IoT LAB BLOCK 3 ECE DEPT	25-11-2024	A	B	C	D
		26-11-2024	E	F	--	--


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G.PULLA REDDY ENGINEERING COLLEGE (Autonomous):: KURNOOL
Four year B.Tech Degree Examination: November-2024
ELECTRONICS & COMMUNICATION ENGINEERING DEPARTMENT
B. Tech ECE III - SEMESTER (S-23)

Scheme & Schedule for Regular Practical Examinations

<u>BATCH</u>	<u>REGD. NOS</u>								<u>TOTAL</u>
A	249X5A04K9	249X5A04L0	249X5A04L1	249X5A04L2	249X5A04L3	249X5A04L4	249X5A04L5	249X5A04L6	[13]
	249X5A04L7	249X5A04L8	249X5A04L9	249X5A04M0	249X5A04M1				
B	249X5A04M2	249X5A04M3	249X5A04M4	249X5A04M5	249X5A04M6	239X1A0401	239X1A0402	239X1A0403	[13]
	239X1A0404	239X1A0405	239X1A0406	239X1A0407	239X1A0408				
C	239X1A0409	239X1A0410	239X1A0411	239X1A0412	239X1A0413	239X1A0414	239X1A0415	239X1A0416	[13]
	239X1A0417	239X1A0418	239X1A0419	239X1A0420	239X1A0421				
D	239X1A0422	239X1A0423	239X1A0424	239X1A0425	239X1A0426	239X1A0427	239X1A0428	239X1A0429	[13]
	239X1A0430	239X1A0431	239X1A0432	239X1A0433	239X1A0434				
E	239X1A0435	239X1A0436	239X1A0438	239X1A0439	239X1A0440	239X1A0441	239X1A0442	239X1A0443	[13]
	239X1A0444	239X1A0445	239X1A0446	239X1A0447	239X1A0448				
F	239X1A0449	239X1A0450	239X1A0451	239X1A0452	239X1A0453	239X1A0454	239X1A0455	239X1A0456	[13]
	239X1A0457	239X1A0458	239X1A0459	239X1A0460	239X1A0461				
G	239X1A0462	239X1A0464	239X1A0465	239X1A0466	239X1A0467	239X1A0468	239X1A0469	239X1A0470	[14]
	239X1A0471	239X1A0472	239X1A0473	239X1A0474	239X1A0475	239X1A0476			
H	239X1A0477	239X1A0478	239X1A0479	239X1A0480	239X1A0481	239X1A0482	239X1A0483	239X1A0484	[14]
	239X1A0485	239X1A0486	239X1A0487	239X1A0488	239X1A0489	239X1A0490			
I	239X1A0491	239X1A0492	239X1A0493	239X1A0494	239X1A0495	239X1A0496	239X1A0497	239X1A0498	[14]
	239X1A0499	239X1A04A0	239X1A04A1	239X1A04A2	239X1A04A3	239X1A04A5			
J	239X1A04A6	239X1A04A9	239X1A04B0	239X1A04B1	239X1A04B2	239X1A04B3	239X1A04B4	239X1A04B5	[14]
	239X1A04B6	239X1A04B7	239X1A04B8	239X1A04B9	239X1A04C1	239X1A04C2			
K	239X1A04C3	239X1A04C4	239X1A04C5	239X1A04C6	239X1A04C7	239X1A04C8	239X1A04C9	239X1A04D0	[14]
	239X1A04D1	239X1A04D2	239X1A04D3	239X1A04D4	239X1A04D5	239X1A04D6			
L	239X1A04D7	239X1A04D8	239X1A04D9	239X1A04E0	239X1A04E1	239X1A04E2	239X1A04E3	239X1A04E4	[14]
	239X1A04E5	239X1A04E6	239X1A04E7	239X1A04E8	239X1A04E9	239X1A04F0			
M	239X1A04F1	239X1A04F2	239X1A04F3	239X1A04F4	239X1A04F5	239X1A04F6	239X1A04F7	239X1A04F8	[13]
	239X1A04F9	239X1A04G0	239X1A04G1	239X1A04G2	239X1A04G3				
N	239X1A04G4	239X1A04G5	239X1A04G6	239X1A04G7	239X1A04G8	239X1A04G9	239X1A04H0	239X1A04H1	[13]
	239X1A04H2	239X1A04H3	239X1A04H4	239X1A04H5	239X1A04H6				
O	239X1A04H7	239X1A04H8	239X1A04H9	239X1A04J0	239X1A04J1	239X1A04J2	239X1A04J3	239X1A04J4	[13]
	239X1A04J5	239X1A04J6	239X1A04J7	239X1A04J8	239X1A04J9				
P	239X1A04K0	239X1A04K1	239X1A04K2	239X1A04K3	239X1A04K4	239X1A04K5	239X1A04K6	239X1A04K7	[12]
	239X1A04K8	229X1A04A0	229X1A04G1	229X1A04J1					[12]
Total									213

Name of the Practical examination	Venue	Date	8.30AM-11.30AM	10.30AM-1.30PM	12.30PM-3.30PM	2.30PM-5.30PM
Digital Circuits & Signal Simulation Lab DCSS (P)	VLSI LAB BLOCK 1	27.11.2024	A	B	C	D
		28.11.2024	E	F	G	H
		29.11.2024	I	J	K	L
		30.11.2024	M	N	O	P
Electronic Devices & Circuits Lab EDC (P)	AEC LAB BLOCK 2	27.11.2024	E	F	G	H
		28.11.2024	I	J	K	L
		29.11.2024	M	N	O	P
		30.11.2024	A	B	C	D
Python Programming Lab PYP (P)	IOT LAB BLOCK 3	27.11.2024	I	J	K	L
		28.11.2024	M	N	O	P
		29.11.2024	A	B	C	D
		30.11.2024	E	F	G	H


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G.PULLA REDDY ENGINEERING COLLEGE (Autonomous):: KURNOOL
Four Year B.Tech Degree Examination: November, 2024
ELECTRONICS & COMMUNICATION ENGINEERING DEPARTMENT
B. Tech ECE V- SEMESTER (S-20)
Scheme & Schedule for Regular Practical Examinations

<u>BATCH</u>	<u>REGD. NOS</u>	<u>TOTAL</u>
A	239X5A04J4 239X5A04J5 239X5A04J6 239X5A04J7 239X5A04J8 239X5A04J9 239X5A04K0 239X5A04K1 239X5A04K2 239X5A04K3 239X5A04K4 239X5A04K5 239X5A04K6	[13]
B	239X5A04K7 239X5A04K8 239X5A04K9 239X5A04L0 239X5A04L1 239X5A04L2 239X5A04L3 239X5A04L4 239X5A04L6 239X5A04L7 239X5A04L8 239X5A04L9 239X5A04M0	[13]
C	239X5A04M1 239X5A04M2 239X5A04M3 239X5A04M4 239X5A04M5 239X5A04M6 229X1A0401 229X1A0402 229X1A0403 229X1A0404 229X1A0405 229X1A0406 229X1A0407	[13]
D	229X1A0408 229X1A0409 229X1A0410 229X1A0411 229X1A0412 229X1A0413 229X1A0414 229X1A0415 229X1A0416 229X1A0417 229X1A0418 229X1A0419 229X1A0420	[13]
E	229X1A0421 229X1A0422 229X1A0423 229X1A0424 229X1A0425 229X1A0426 229X1A0427 229X1A0428 229X1A0429 229X1A0430 229X1A0431 229X1A0432 229X1A0433	[13]
F	229X1A0434 229X1A0436 229X1A0437 229X1A0438 229X1A0439 229X1A0440 229X1A0441 229X1A0442 229X1A0443 229X1A0444 229X1A0446 229X1A0447 229X1A0448	[13]
G	229X1A0449 229X1A0450 229X1A0451 229X1A0452 229X1A0453 229X1A0454 229X1A0455 229X1A0456 229X1A0457 229X1A0458 229X1A0459 229X1A0460 229X1A0461 229X1A0462	[14]
H	229X1A0463 229X1A0464 229X1A0465 229X1A0466 229X1A0467 229X1A0468 229X1A0469 229X1A0470 229X1A0471 229X1A0472 229X1A0473 229X1A0474 229X1A0475 229X1A0476	[14]
I	229X1A0477 229X1A0478 229X1A0479 229X1A0480 229X1A0481 229X1A0482 229X1A0483 229X1A0484 229X1A0485 229X1A0486 229X1A0488 229X1A0489 229X1A0490 229X1A0491	[14]
J	229X1A0492 229X1A0493 229X1A0494 229X1A0495 229X1A0496 229X1A0497 229X1A0498 229X1A0499 229X1A04A1 229X1A04A2 229X1A04A3 229X1A04A4 229X1A04A5 229X1A04A6	[14]
K	229X1A04A7 229X1A04A8 229X1A04A9 229X1A04B0 229X1A04B1 229X1A04B2 229X1A04B3 229X1A04B4 229X1A04B5 229X1A04B6 229X1A04B7 229X1A04B8 229X1A04B9 229X1A04C0	[14]
L	229X1A04C1 229X1A04C2 229X1A04C3 229X1A04C4 229X1A04C5 229X1A04C6 229X1A04C7 229X1A04C8 229X1A04C9 229X1A04D0 229X1A04D1 229X1A04D2 229X1A04D3	[13]
M	229X1A04D4 229X1A04D5 229X1A04D6 229X1A04D7 229X1A04D8 229X1A04D9 229X1A04E0 229X1A04E1 229X1A04E2 229X1A04E3 229X1A04E4 229X1A04E5 229X1A04E6	[13]
N	229X1A04E7 229X1A04E8 229X1A04E9 229X1A04F0 229X1A04F1 229X1A04F2 229X1A04F3 229X1A04F4 229X1A04F5 229X1A04F6 229X1A04F7 229X1A04F8 229X1A04F9	[13]
O	229X1A04G0 229X1A04G2 229X1A04G3 229X1A04G4 229X1A04G5 229X1A04G6 229X1A04G7 229X1A04G8 229X1A04G9 229X1A04H0 229X1A04H1 229X1A04H2 229X1A04H3	[13]
P	229X1A04H4 229X1A04H5 229X1A04H6 229X1A04H7 229X1A04H8 229X1A04H9 229X1A04J0 229X1A04J2 229X1A04J3 229X1A04F2 229X1A04F7 229X1A04G4	[12]

Total 212

Name of the Practical examination	Venue	Date	8.30AM -11.30AM	10.30AM -1.30PM	12.30PM -3.30PM	2.30PM -5.30PM
Microprocessors & Microcontrollers Lab (MPMC(P))	Microprocessors Lab BLOCK 2	27.11.2024	A	B	C	D
		28.11.2024	E	F	G	H
		29.11.2024	I	J	K	L
		30.11.2024	M	N	O	P
Linear & Digital IC Applications Lab (LDICA(P))	IC Applications Lab BLOCK 2	27.11.2024	E	F	G	H
		28.11.2024	I	J	K	L
		29.11.2024	M	N	O	P
		30.11.2024	A	B	C	D
PCB Design Lab PCB(P)	IOT LAB BLOCK 3	27.11.2024	I	J	K	L
		28.11.2024	M	N	O	P
		29.11.2024	A	B	C	D
		30.11.2024	E	F	G	H


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G.PULLA REDDY ENGINEERING COLLEGE (Autonomous) :: KURNOOL
Four year B. Tech Degree Examination: November, 2024
ELECTRONICS & COMMUNICATION ENGINEERING DEPARTMENT
B.TECH ECE VI- SEMESTER (S-20)
Scheme of schedule for Supplementary Practical examinations

<u>BATCH</u>	<u>REGD. NOS</u>	<u>TOTAL</u>
A	219X1A04D3 219X1A04E5 219X1A04H3	03
B	219X1A04C2	01
C	219X1A0437	01

Name of the Practical examination	Venue	Date	Time
			(9.00AM - 12.00Noon)
E-CAD Lab ECAD(P)	VLSI LAB BLOCK 1	25.11.2024	A
Digital Signal Processing Lab DSP(P)	SIMULATION LAB BLOCK 1	26.11.2024	B
Microwave and Optical Communications lab MOC(P)	MWE LAB BLOCK 2 FIRST FLOOR	26.11.2024	C
Mobile App Development and Embedded System Programming Lab MADESP(P)	IOT LAB BLOCK 3	27.11.2024	C

P.R.
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G.PULLA REDDY ENGINEERING COLLEGE (Autonomous) :: KURNOOL
Four year B. Tech Degree Examination: November, 2024
ELECTRONICS & COMMUNICATION ENGINEERING DEPARTMENT
B.TECH ECE VII- SEMESTER (S-20)
Scheme of schedule for Supplementary Practical examinations

<u>BATCH</u>	<u>REGD. NOS</u>	<u>TOTAL</u>
A	209X1A04J8	01

Name of the Practical examination	Venue	Date	Time
			(9.00AM - 12.00 Noon)
Summer Internship-2(SI-2(P))	R & D Lab	25.11.2024	A

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G.PULLA REDDY ENGINEERING COLLEGE (Autonomous) :: KURNOOL
Four year B. Tech Degree Examination: November, 2024
ELECTRONICS & COMMUNICATION ENGINEERING DEPARTMENT
B.TECH ECE III- SEMESTER (S-20)
Scheme of schedule for Supplementary Practical examinations

<u>BATCH</u>	<u>REGD. NOS</u>	<u>TOTAL</u>
A	229X1A04E7	01
B	229X1A04B4	01

Name of the Practical examination	Venue	Date	Time
			(9.00AM - 12.00 Noon)
Digital System Design Lab DSD(P)	PDC LAB BLOCK 2	25.11.2024	A
Basic Simulation Lab BS(P)	SIMULATION LAB BLOCK 1	25.11.2024	B

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Four year B. Tech Degree Examination: November, 2024
ELECTRONICS & COMMUNICATION ENGINEERING DEPARTMENT
B.TECH ECE IV- SEMESTER (S-20)
Scheme of schedule for Supplementary Practical examinations

<u>BATCH</u>	<u>REGD. NOS</u>	<u>TOTAL</u>
A	229X1A0486 229X1A04B4	02
B	229X1A0456 229X1A0459 229X1A04J2	03
C	229X1A04B4	01

Name of the Practical examination	Venue	Date	Time
			(9.00AM - 12.00 Noon)
Analog and Digital Communications Lab ADCM(P)	COMM. LAB BLOCK 2	26.11.2024	A
Networks and Transmission Lines Lab NTL(P)	EEE BLOCK	25.11.2024	B
Soft Skills Lab SS(P)	T & P ONLINE MODE	29.11.2024	C

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