G.PULLA REDDY ENGINEERING COLLEGE (Autonomous): KURNOOL M.Tech II – SEMESTER (Scheme-2022) FIRST SESSIONAL EXAMINATIONS – MAY–2025

Time: 10:00 A.M. to 12.00 NOON.

Date	Day	Paper No.	Computer Science & Engineering	Structural Engineering	VLSI and Embedded Systems
05-05-2025	MON	1	MC -Mobile Computing	ARCD - Advanced Reinforced Concrete Design	AICD — Analog IC Design
06-05-2025	TUE	2	DS – Data Science	FEM - Finite Element Methods	EIOT - Embedded Systems for Internet of Things
07-05-2025	WED	3	Elective — III - ML- Machine Learning	Elective — III : SS - Stability of Structures	Elective - III : CVD - CAD for VLSI Design
08-05-2025	THU	4	Elective - IV -SC -Soft Computing	Elective — IV: ERDS - Earthquake Resistant Design of Structures	Elective – IV : MESD - Microcontrollers for Embedded System Design
09-05-2025	FRI	5	Elective - V- DP Design Patterns	Elective – V : SD- Structural Dynamics	Elective – V: VDFT - VLSI Design for Testability

Note: - No grace period for entering into the examination hall during the Sessional and End exams.

Students are allowed into halls 15 minutes before commencement of examinations.

PRINCIPAL